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XB	V	5 629 733	11/29/94	Youman et al.		348	7		
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XB		0 624 039 A2	11/09/94	EP					
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χß									
_		An overview of the phase-locked loop (PLL); Application Note/AN177, Philip							
χβ		Semiconductors; December 1991; pgs. 1-6 Circuit Description of the NE564; Application Note/AN 179; Philips Semiconductors;							
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rB		Frequency thesis with the NE564; Philips Semiconductors; December 1988; pgs. 1-3							
X.B		Clock regenerator with crystal-controlled phase-locked loop VCO (NE564); Philips Semiconductors December 1991. pgs. 1-10							
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